

SUCHIN
10/708,039

REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Papadopoulou et al.

Conf. No.: 2038

Serial No.: 10/708,039

Art Unit: 2825

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Examiner: Parihar, Suchin

Title: IC DESIGN MODELING ALLOWING
DIMENSION-DEPENDENT RULE CHECKING

Docket No.: BUR920030149US1
(IBMB-0034)

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S.P.
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AFTER-FINAL AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS:

This paper is being filed in response to the Office Action dated July 30, 2007. Please reconsider the above-referenced patent application in view of the following amendments and remarks:

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